Memo

To: Micah Taylor

From: Group D Section 4

Date: October 29, 2014

Re: Milestone 4

Dear Professor,

For Milestone 4, a control unit was created by Bo, along with it testbench by Donai. Bo did the State transition diagram. The components were implemented in Xilinx by Bo, Yicheng and An. Bo did all of the Xilinx work that assigned to YiCheng. (Register File and 16bit ALU, since YiCheng dropped the course). Also, Bo did the Shift left 8, Zero Extended, Sign Extended and Mux. An Hu implemented all the registers with different size(16bits; 11bits; 8bits; 3bits; 1bit) . Bo wrote the “test necessary to verify the correct implementation of the control unit”. Also, Jackie overlooked documentation and the integration plan. The control test which wasn’t required for this milestone is close to completion but not yet finished. All things for Milestone 4 are completed.

Sincerely,

Group 4D